ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention

FAST-DISABLED VOLTAGE REGULATOR CIRCUIT WITH LOW-NOISE FEEDBACK LOOP AND OPERATING METHOD THEREOF

Submision Type : Utility Patent Filing

Application Number:

10/711618

EFS ID: 69453

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First Named Applicant: Ling-Wei Ke
Attorney Docket Number: MTKP0080USA

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File Listing:

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